



PATENT  
005586-20026 (81784.0018)

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#16  
VT  
3/11/B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

D2  
3/12/03  
In re application of:

Akira TSUKIHASHI

Serial No: 09/476,862

Filed: January 3, 2000

For: DATA PROCESSING CIRCUIT FOR  
TEMPORARILY SUSPENDING DATA  
RECORDING ONTO A DISK

Art Unit: 2653

Examiner: Gautam Patel

I hereby certify that this correspondence  
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October 7, 2002

Date of Deposit

John P. Scherlach, Reg. No. 23,009

Name

John P. Scherlach 10/07/02

Date

**AMENDMENT**

Box Non-Fee Amendment  
Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated August 6, 2002, please amend the  
above-identified application as follows:

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Technology Center 2600

**IN THE CLAIMS:**

Rewrite claim 3 as follows:

3. (Three Times Amended) A recording data processing circuit for  
processing received data sent at a slower data transmission speed than a data  
processing speed at which to write recording data onto a non-erasable, write-once  
disk, comprising:

a buffer memory for temporarily storing the received data;  
a data processing circuit for preparing the recording data to record  
onto the disk, based on the received data read from the buffer memory;

a system control circuit for controlling writing and reading of the  
received data with respect to the buffer memory, and operation of the data  
processing circuit, and

a writing circuit for writing the recorded data supplied from the data  
processing circuit, onto the disk,